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US 5929677 A	19990727	Phase locked loop having a voltage source responsive to a filtered phase difference output voltage and semiconductor device including the phase locked loop	327/157	327/147; 327/148; 327/156; 331/17	Murata; Toshihiko
US 5923569 A	19990713	Method for designing layout of semiconductor integrated circuit semiconductor integrated circuit obtained by the same method	716/7	257/E27.108; 716/14; 716/18; 716/4	Kumashiro; Shinichi et al.

		and method for verifying timing thereof			
US 5917217 A	19990629	Lateral field effect transistor and method of manufacturing the same	257/343	257/339; 257/492; 257/E27.064; 257/E29.04; 257/E29.066; 257/E29.256	Kitamura; Akio et al.
US 5916619 A	19990629	Fried instant noodles and method for manufacturing the same	426/557	426/18; 426/28; 426/451; 426/555; 426/622	Miyazaki; Eiji et al.
US 5909758 A	19990608	Supporting tool and method of installing same	16/406	248/205.3; 248/205.6; 248/205.8; 248/206.3; 248/544	Kitamura; Akio
US 5885878 A	19990323	Lateral trench MISFET and method of manufacturing the same	438/302	148/DIG.126; 148/DIG.151; 257/E21.418; 257/E21.419; 257/E29.118; 257/E29.121; 257/E29.256; 257/E29.258; 257/E29.26; 438/297; 438/306; 438/307	Fujishima; Naoto et al.
US 5875148 A	19990223	Semiconductor memory	365/230.06	257/E21.659; 257/E27.097; 365/189.11	Tanaka; Yasuhiro et al.
US 5844275 A	19981201	High withstand-voltage lateral MOSFET with a trench and method of producing the same	257/335	257/336; 257/343; 257/E21.417; 257/E29.021; 257/E29.256; 257/E29.258; 257/E29.268	Kitamura; Akio et al.
US 5830981 A	19981103	Process for producing polyethylene terephthalate	528/283	524/714; 524/765; 524/773; 528/274; 528/308.2; 528/308.4;	Koreishi; Hiroshi et al.

				528/308.5; 528/308.6; 528/499; 528/502R; 528/503	
US 5824951 A	19981020	Electronic part comprising a casing with a narrow groove formed in the casing	174/564	257/E23.193; 310/348; 361/728	Tanaka; Yasuhiro
US 5814806 A	19980929	Code sheet representing multiple code information and method for producing the same	235/494	235/462.01	Tanaka; Yasuhiro et al.
US 5781466 A	19980714	Semiconductor memory with built-in cache	365/49	365/149; 365/230.09	Tanaka; Yasuhiro et al.
US 5760440 A	19980602	Back-source MOSFET	257/328	257/329; 257/330; 257/334; 257/336; 257/492; 257/493; 257/E27.064; 257/E29.021; 257/E29.119; 257/E29.256; 257/E29.26; 257/E29.262; 257/E29.267; 257/E29.268	Kitamura; Akio et al.
US 5757216 A	19980526	Electronic device using phase synchronous circuit	327/156	327/147; 331/14; 331/17	Murata; Toshihiko
US 5737237 A	19980407	Method and apparatus for data path circuit layout design and memory medium for causing computer to execute data path circuit layout design	716/9	716/8	Tanaka; Yasuhiro et al.
US 5705842 A	19980106	Horizontal MOSFET	257/362	257/281; 257/335; 257/343; 257/358; 257/360; 257/361; 257/363; 257/373; 257/379; 257/449; 257/473;	Kitamura; Akio et al.

				257/E29.258	
US 5701026 A	19971223	Lateral trench MISFET	257/510	257/335; 257/374; 257/E21.418; 257/E21.419; 257/E29.118; 257/E29.121; 257/E29.256; 257/E29.258; 257/E29.26	Fujishima; Naoto et al.
US 5684641 A	19971104	Objective lens and optical head apparatus using the objective lens	359/719	359/710; 359/711; 369/112.23	Tanaka; Yasuhiro et al.
US 5659258 A	19970819	Level shifter circuit	326/68	326/81; 326/83	Tanabe; Tetsuya et al.
US 5652727 A	19970729	Semiconductor memory device	365/203	365/202	Tanaka; Yasuhiro et al.
US 5651006 A	19970722	Hierarchical network management system	370/408	370/902; 709/223; 710/120	Fujino; Shuji et al.
US 5636048 A	19970603	Equalizing amplifier, receiver using the same and preamplifier	398/202	330/278; 330/291; 330/304; 398/208	Kogure; Kazuhisa et al.
US 5633525 A	19970527	Lateral field effect transistor	257/381	257/141; 257/343; 257/401; 257/E23.015; 257/E29.026; 257/E29.054; 257/E29.064; 257/E29.12; 257/E29.268	Kitamura; Akio et al.
US 5631307 A	19970520	Photopolymerization initiator composition and photopolymerizable composition	522/25	522/31	Tanaka; Yasuhiro et al.
US 5596521 A	19970121	Semiconductor memory with built-in cache	365/49	365/149	Tanaka; Yasuhiro et al.
US 5591657 A	19970107	Semiconductor apparatus manufacturing method employing gate side wall self-aligning for masking	438/202	257/E21.417; 257/E21.696; 257/E29.04; 257/E29.066; 257/E29.133; 257/E29.256; 257/E29.268; 438/208;	Fujishima; Naoto et al.

				438/286; 438/305	
US 5586255 A	19961217	Network management operation system and network management operation method	709/223	715/700	Tanaka; Yasuhiro et al.
US 5577223 A	19961119	Dynamic random access memory (DRAM) with cache and tag	711/118	365/230.03; 365/49; 711/105	Tanoi; Satoru et al.
US 5573888 A	19961112	Image transfer method	430/256	430/257; 430/273.1	Imabayashi; Arata et al.
US 5566115 A	19961015	Semiconductor memory device	365/203	369/180; 369/202; 369/226	Tanaka; Yasuhiro et al.
US 5535058 A	19960709	Focus error detecting element and optical head using the same	359/711	359/719	Tanaka; Yasuhiro et al.
US 5523599 A	19960604	High voltage MIS field effect transistor	257/327	257/335; 257/343; 257/401; 257/653; 257/E29.012; 257/E29.027; 257/E29.12; 257/E29.256; 257/E29.258	Fujishima; Naoto et al.
US 5511030 A	19960423	Semiconductor memory device and method of driving same	365/203	365/190	Tanaka; Yasuhiro et al.
US 5502323 A	19960326	Lateral type field effect transistor	257/335	257/341; 257/401; 257/E23.145; 257/E23.152; 257/E29.027; 257/E29.12; 257/E29.122; 257/E29.133; 257/E29.256; 257/E29.268	Kitamura; Akio et al.
US 5477496 A	19951219	Semiconductor memory device having circuits for precharging and equalizing	365/203	365/202	Tanaka; Yasuhiro et al.
US 5473471 A	19951205	Complex lens with diffraction grating	359/569	359/566; 359/571; 359/574; 359/741; 359/742; 369/112.08	Yamagata; Michihiro et al.
US 5471399	19951128	Network management system	716/11	370/241;	Tanaka;

A		and network status display method		714/47; 714/57; 715/733; 715/839; 715/969; 716/8	Yasuhiro et al.
US 5461259 A	19951024	High-current integrated circuit	257/758	257/401; 257/776; 257/E27.06	Kitamura; Akio et al.
US 5451671 A	19950919	Method of purifying 2',3'-dideoxynucleosides	536/27.12	536/27.14; 536/28.2	Shiragami; Hiroshi et al.
US 5438665 A	19950801	Direct memory access controller for handling cyclic execution of data transfer in accordance with stored transfer control information	710/25	710/28	Taniai; Takayoshi et al.
US 5436486 A	19950725	High voltage MIS transistor and semiconductor device	257/378	257/140; 257/146; 257/577; 257/E29.064; 257/E29.197	Fujishima; Naoto et al.
US 5432370 A	19950711	High withstand voltage M I S field effect transistor and semiconductor integrated circuit	257/339	257/336; 257/337; 257/343; 257/401; 257/409; 257/E27.06; 257/E29.012; 257/E29.015; 257/E29.017; 257/E29.027; 257/E29.066; 257/E29.256; 257/E29.258	Kitamura; Akio et al.
US 5414917 A	19950516	Method of manufacturing piezoelectric resonator chips	29/25.35	310/340	Tanaka; Yasuhiro
US 5317792 A	19940607	Method of manufacturing piezoelectric resonator	29/25.35	29/411	Tanaka; Yasuhiro
US 5310895 A	19940510	Method for production of nucleoside derivatives by selective hydrolysis	536/27.14	536/103; 536/27.6; 536/27.62; 536/27.63; 536/27.81; 536/28.2; 536/28.5; 536/28.53;	Shiragami; Hiroshi et al.

				536/28.54	
US 5270444 A	19931214	Process for treatment of polyethylene terephthalate, polyethylene terephthalate for molding purposes and process for preparation thereof	528/499	528/308.2; 528/308.4; 528/308.5; 528/503	Shiraki; Shigemi et al.
US 5241046 A	19930831	Process for treatment of polyethylene terephthalate, polyethylene terephthalate for molding purposes and process for preparation thereof	528/499	528/308.2; 528/308.4; 528/308.5; 528/503	Shiraki; Shigemi et al.
US 5213166 A	19930525	Pile driving and pile removing method	173/1	175/65	Watanabe; Naotaka et al.
US 5159302 A	19921027	Ladder type band-pass filter comprising of assembled plural vibrator blocks	333/189	310/348; 310/355	Tanaka; Yasuhiro
US 5119487 A	19920602	DMA controller having programmable logic array for outputting control information required during a next transfer cycle during one transfer cycle	710/22		Taniai; Takayoshi et al.
US 5068849 A	19911126	Cyclic data transmission method	370/509	370/452	Tanaka; Yasuhiro
US 5043935 A	19910827	Data transfer system for rearranging data units using intermediate temporary registers	710/66		Taniai; Takayoshi et al.
US 5033017 A	19910716	Programmable logic array with reduced power consumption	708/232	326/105; 326/38; 326/44	Taniai; Takayoshi et al.
US 4998246 A	19910305	Method for transmission of cyclic data	370/452		Tanaka; Yasuhiro
US 4980890 A	19901225	Semiconductor integrated circuit	714/735		Taniai; Takayoshi et al.
US 4927247 A	19900522	Objective lens for optical disk system and optical head using the same	359/708		Tanaka; Yasuhiro et al.
US 4842388 A	19890627	Single optical lense with diffraction limit converging capability and wide field angle	359/718		Tanaka; Yasuhiro et al.
US 4786772 A	19881122	Induction heating coil for heat-treating metallic tubes	219/643	219/632; 219/674; 266/129	Umemoto; Tadahiro et al.
US 4695756 A	19870922	Laminated terminal layer for electronic component	310/355	310/326; 310/354; 310/364	Tanaka; Yasuhiro

US 4693571 A	19870915	Support and drive unit for mirror devices	359/874	359/877	Kimura; Sakae et al.
US D291461 S	19870818	Reconfigurable toy vehicle	D21/582		Kitamura; Akio
US D290723 S	19870707	Reconfigurable toy vehicle	D21/581		Kitamura; Akio
US D290630 S	19870630	Reconfigurable toy tank	D21/580		Kitamura; Akio
US D290481 S	19870623	Reconfigurable toy boat	D21/580		Kitamura; Akio
US 4656385 A	19870407	Housing having grooves therein for mounting a piezoelectric ladder filter	310/348	174/549; 174/560; 333/189	Tanaka; Yasuhiro
US 4641055 A	19870203	Piezoelectric resonator with notched sides	310/368	310/312	Tanaka; Yasuhiro
US 4582469 A	19860415	Intake valve apparatus in air pump	417/571	137/855	Asaka; Urataro et al.
US 4551074 A	19851105	Air pump apparatus	417/366	417/415; 417/440; 92/158	Asaka; Urataro et al.
US 4323865 A	19820406	Ladder-type piezoelectric filter	333/187	310/348; 333/189; 333/190	Tanaka; Yasuhiro et al.